REMARKS

Claims 4 and 11-15 stand rejected under 35 U.S.C. § 103 as unpatentable over Lofgren et al. The Examiner's rejection in this ground is respectfully traversed.

The claims have been extensively amended to both broaden and improve the form thereof. These amendments do not narrow any of the limitations of the claims.

Claims 11 and 13, both require a PLL frequency synthesizer circuit wherein "when the control voltage changes to a value close to one of the driving limits, the power supply voltage is modified *independently of the control voltage* so as to maintain the stability of the output signal from the VCO." Claims 12 and 14 both require the use of the PLL frequency synthesizer wherein "the power supply signal (applied to the VCO) is *independent* of the control signal to thereby widen an apparent lock range of the PLL." Claim 4 depends from claim 12 and claim 15 depends from claim 14 and include all of the respective limitations found therein.

The Lofgren et al. reference discloses that the control voltage and the auto adjust applied to the VCO 42 be modified as a function of, rather than independently of, each other. Accordingly, Lofgren et al. neither discloses nor suggests the invention as defined in the claims. Accordingly, these claims are believed to be in condition for allowance.

Enclosed herewith are a substitute set of drawings for this application.

It is requested that the Examiner acknowledge the Information Disclosure Statement ("IDS") dated May 3, 2001. A copy of the Patent Office postcard acknowledging receipt of this IDS is attached hereto. Also attached is a copy of the Applicant's Art Citation form. It is requested that the Examiner initial and return the form to the undersigned.

Reconsideration and allowance of the application are earnestly solicited.

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Respectfully submitted,

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Attachments